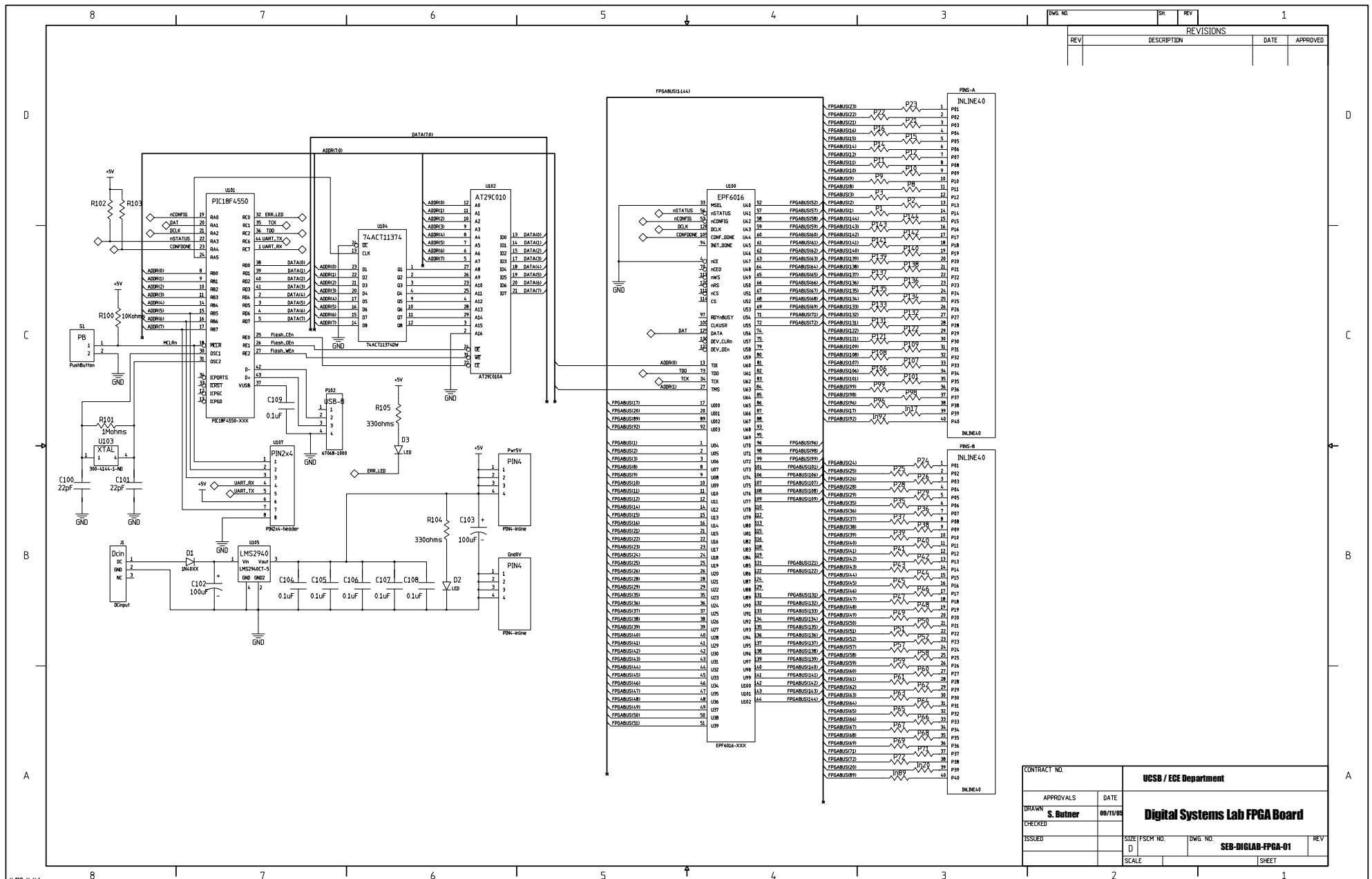


DWG. NO.	SR	REV	1
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



CONTRACT NO.		UCSB / ECE Department	
APPROVALS	DATE	DRAWN <b>S. Butner</b> 08/17/06	
CHECKED		<b>Digital Systems Lab FPGA Board</b>	
ISSUED		SIZE: FSCH NO. D	DWG. NO. <b>SEB-DIGLAB-FPGA-01</b>
		SCALE	REV SHEET